

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(bus adj interface adj (logic or model)) and (memory adj intercae adj (logic or model)) and (hardware same software same co\$1simulation)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/02/11 15:11
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L10	204	I9 and bus and memory	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/02/11 15:38
L11	146	I9 and ((interfac\$4 or connect\$4 or designat\$4 or allocat\$4 or assign\$4 or rout\$4 of layout\$3) with bus) and ((interfac\$4 or connect\$4 or designat\$4 or allocat\$4 or assign\$4 or rout\$4 of layout\$3) with memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/02/11 15:41

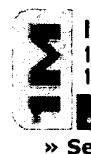
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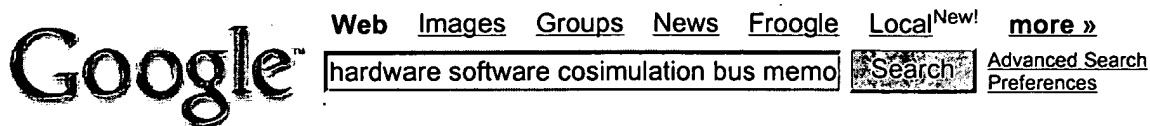
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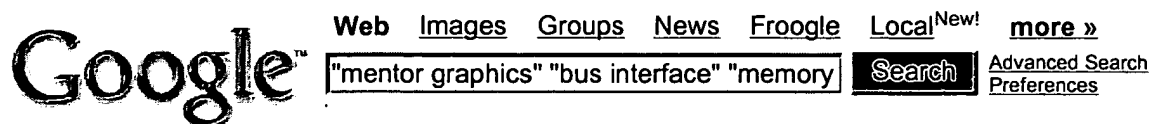
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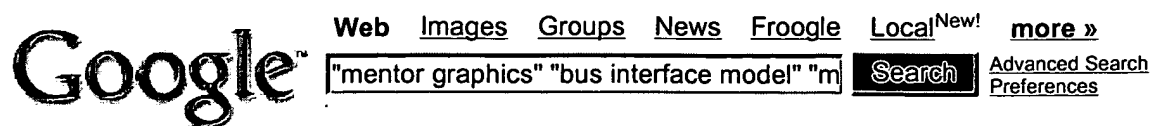
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